



Chronicles From the Book of MOSA: The Journey Continues

Ken Grob – Elma Electronic
Embedded Tech Trends 2023

- **Chronicling the MOSA Journey**
 - A little bit of background
 - Initiatives that the Services are conducting
 - Efforts of the SOSA Consortium & industry to support those initiatives
- **Trends**
- **What's next**



Founded in 1960



800+ employees worldwide



Annual revenue of \$150 Million (2021)



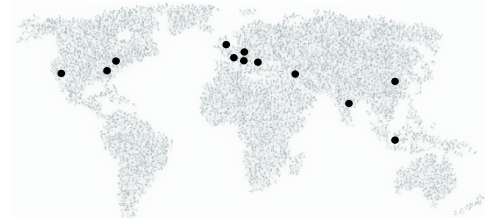
Global leader in embedded computing solutions, chassis, embedded boards, enclosures, 19” cabinets



Worldwide manufacturing, design, and sales



Solution partner of choice: technical expertise, precision engineering, reliability, and long-term support



Bringing Rugged High-Performance Computing and Networking to Challenging Applications



Rugged standards-based systems



Extended temperature operation



IP67
IP69

IP67/69 safe against water, sand, dust, and salt-fog intrusion



High shock and vibration environment

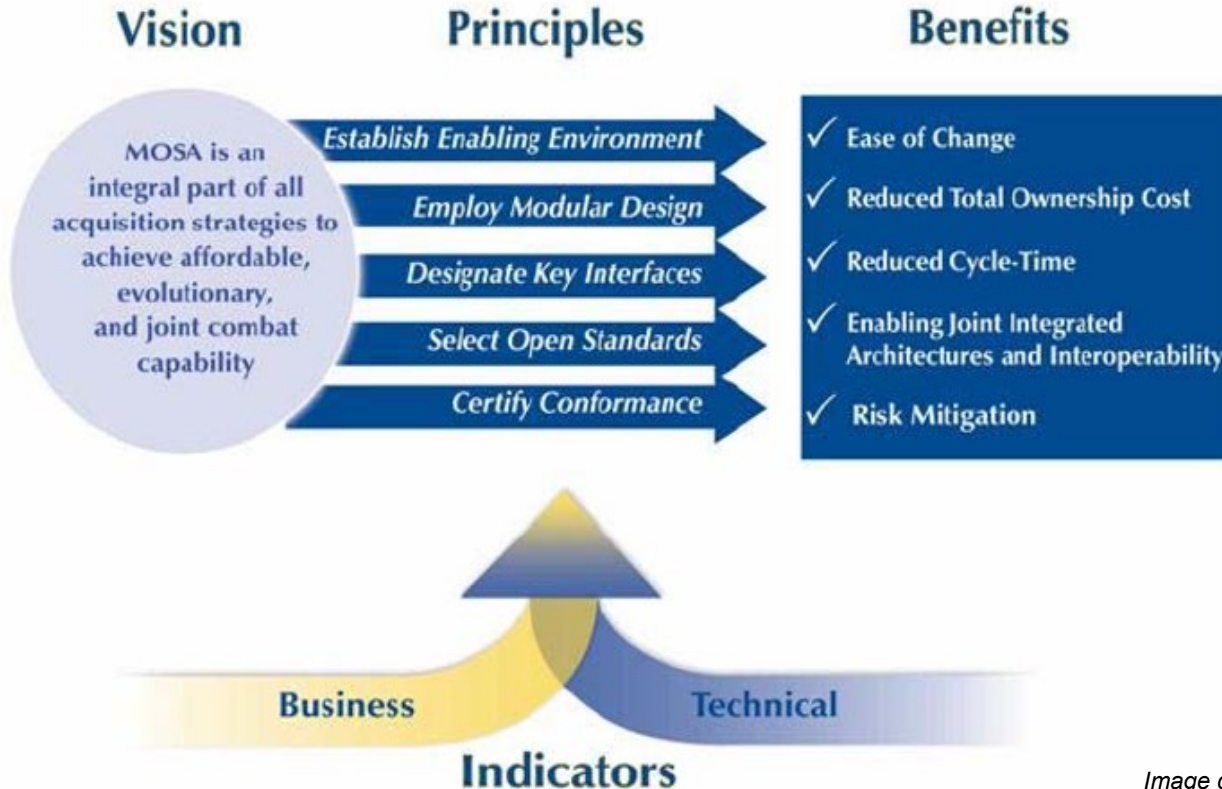


High-performance sensor processing, automated control, and deep learning



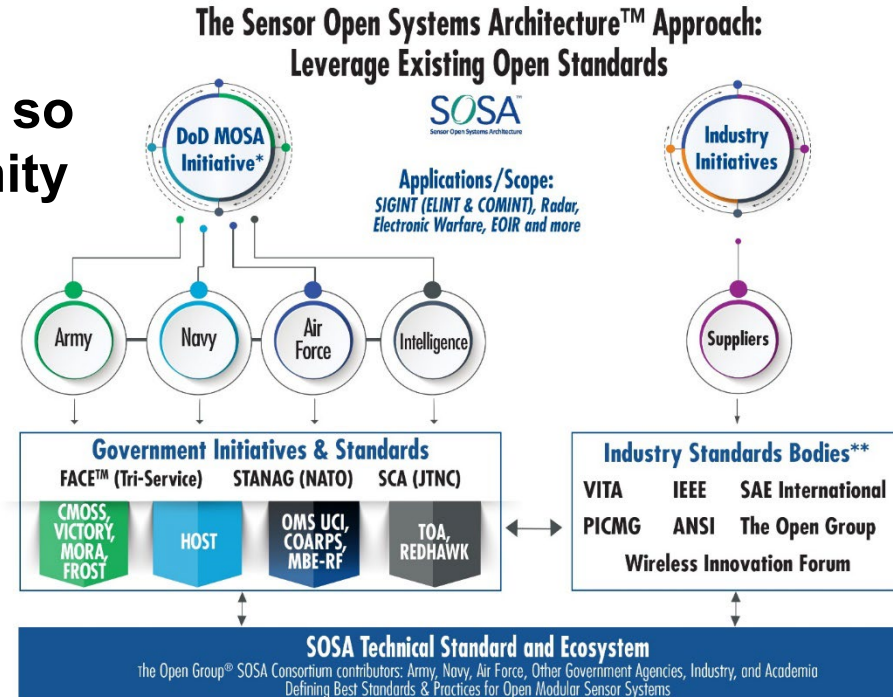
Modular Open Systems Approach

Fundamental Building Blocks



SOSA by the Numbers

- **144 members as of January 2023 (74 members in 2019)**
 - Includes the Air Force, Navy, Army, other government, agencies, industry and academia
- **How and why these initiatives are so important to the defense community**
 - Building better platforms
 - Collaborative environment between government and industry
 - Ensure interoperability
 - Mix and match vendors





SOSA Technical Standard

Published:

- Edition 1.0 - Sept 2021
- Snapshot 1 v. 2.0 - Aug 2022

In process:

- Snapshot 2 v. 2.0 in process

Support Documents

Published:

- Business Guide
- Supplier Guide
- Marketing Guide

In process:

- Reference Implementation Guide
- Acquisition & Contracting Guide

Elma's Participation in SOSA

- **Valerie Andrew**
 - Senior Strategic Marketing
 - SOSA BWG Outreach Chair
- **Mark Littlefield**
 - Senior Manager, Embedded Computing Solutions
 - SOSA TWG SFF SC Chair
- **Ken Grob**
 - Director of Embedded Technology
 - SOSA TWG Hardware Committee
- **Other members and staff:**
 - Dave C., Ovidiu M., Gary Hanson, Ram R., etc.



Elma Partnering with the SOSA Ecosystem

Technologies, customer collaborations

ELMA
Your Solution Partner



2022 MOSA Technology Demonstrations

Showcasing an ecosystem of implementations of products aligned to the SOSA Standard at multiple events



TSOA-ID February 2022



FACE SOSA TIM & Expo - September 2022
12 Slot SOSA Aligned CMOSS-2 ATR and E-Frame with TrMA6x
Ethernet Performance & AI Multifunctional Demos



AUSA - October 2022
AI NVIDIA GPGPU Demo



AUSA - October 2022
Ethernet Performance Demo



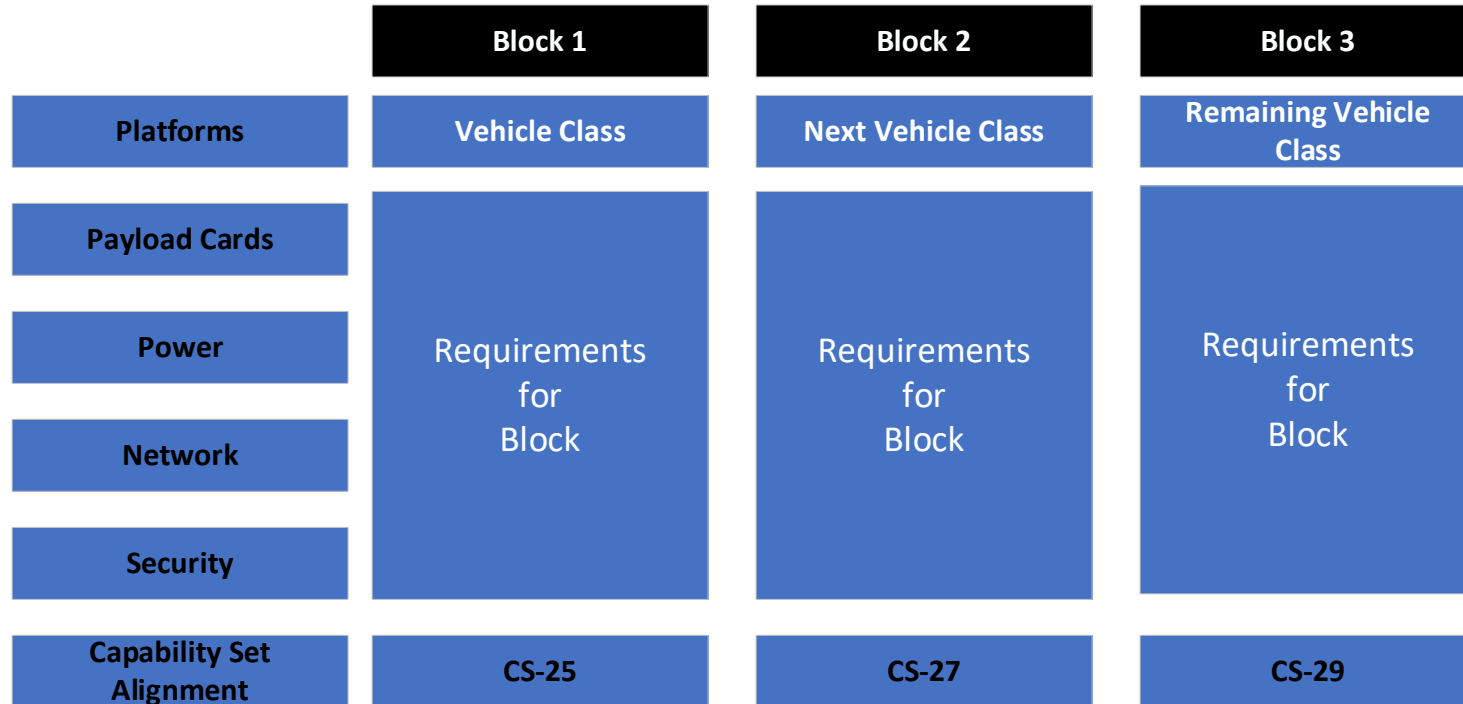
MOSA New Initiatives - The Cavalry is in the Lead!



US Army CMFF Reference Architecture for Vehicles and Aircraft

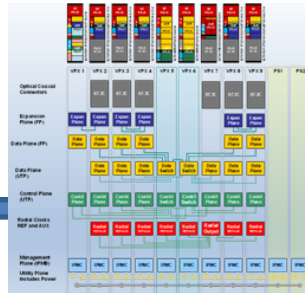
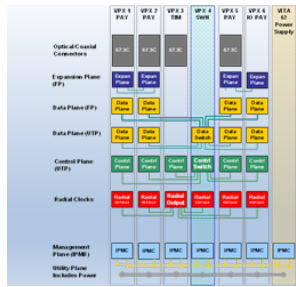
- **US Army Technical Exchange Meetings** (TEM 8 and TEM 9)
- **CMFF RFI: CMOSS Mounted Form Factor** (RFI March-May 2022)
- **SAVE IDD for Box Envelope**
 - Focus on Block One System Requirements CS 25
 - Primes engaged in developing CMFF solutions
 - Programs will adopt CMFF RA as Block Point milestones are accomplished
- **U360:** 360 Degree Situation Awareness and Targeting Program, TEM March, 2022, B-Kit
- **Xtech Competitions**
 - PNT/Chassis/Switch in fall of '21; CMFF- A-KIT in fall of '22
- **Precision Navigation and Timing – Open Innovation Lab (PNT OIL)**
 - APG facility hosts industry suppliers to support test and integration of MOSA solutions

CMFF Phased Development Approach



Incremental Approach Presented by the ARMY at the TSOA-ID 2022

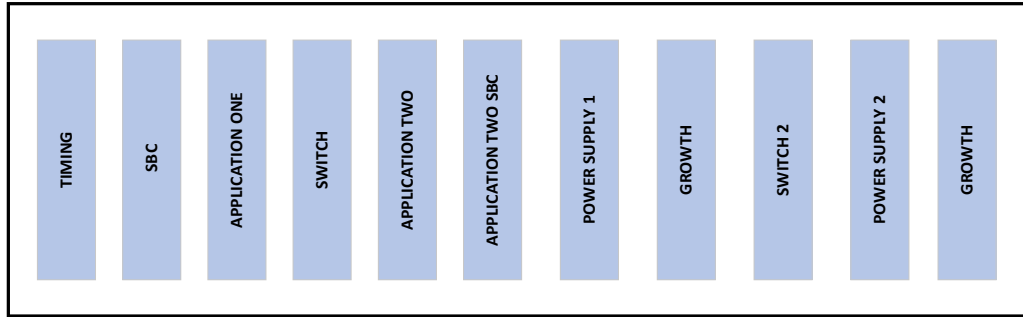
CMFF Forward Path for Ground Vehicles



Elma Backplane Topology Drawings shown for Notional Chassis:

6 slot +
1 PSU
or;
9 Slot +
2 PSU

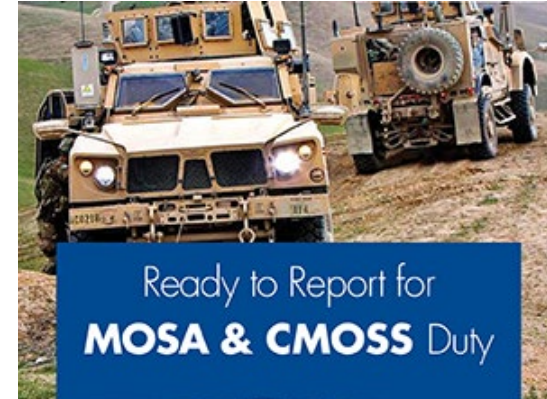
Scalable CMFF Chassis



3 PAYLOAD – 4 SLOT

5 PAYLOAD – 7 SLOT

8 PAYLOAD – 11 SLOT IF SAVE ALIGNED



Ready to Report for
MOSA & CMOSS Duty



SAVE Format CMOSS ATR Chassis Solutions

- **SAVE IDD CMOSS ATR with Tray**
- **CMFF Scalable BP Architecture**
- **12 Slot CMOSS-2 Reference Backplane**
- **Wide Range - VITA 62 Power Supply**
- **Useful for CMFF Payload Testing**
- **Interim Step towards a fully compliant CMFF chassis**



- **SWaP-C**
 - Constrained space
- **TDP constrained to 600 watts total**
- **Wide environmental requirements**
- **Stringent power supply requirements**
 - Must support a wide input voltage range
 - PS filtering for mixed environments
 - Hold-Up required
- **Boxes to be very rugged → driving requirements for:**
 - Shock, vibration, immersion, etc.

VPXtra[®] 400DW-IQI 3U



MOSA New Initiatives – AI Applications in Vehicle Sensors (U360)

- **AI Object Recognition Demo**

- Eight Slot CompactFrame integrated with EIZO GR2 RTX5000 GPGPU with CCT TrMA6x Ice Lake D Multi-Core Compute Intensive SBC
 - System runs an AI Application including a trained neuro-network



- **Ethernet Performance Demo**

- Comparison of Ethernet Performance: 1/10/40Gb vs 1/10/40/100Gb
 - Six Slot CompacFrame
 - TrK9 I/O Intensive Tiger Lake SBC
 - 4590A Ethernet Switch → 4682e Ethernet Switch



AI Demo - Implemented

Trend: GPGPUs Moving from Turing to Ampere Architecture



CompacFrame Development Platform with Partner Ecosystem Plug-In Cards (PICs)

Bring the power of AI and NVIDIA® to rugged embedded computing. This demo shows the power of the SOSA architecture to perform very fast processing of streaming video as well as deep learning through intelligent object detection & recognition.



3U 8-slot OpenVPX backplane

- ▶ All slot profiles aligned to SOSA 1.0
- ▶ Features VITA 67.3 RF and optical I/O modules for high-speed connectivity

Featuring the following Plug-In Cards (PICs)

- ▶ Concurrent Technologies compute-intensive SBC
- ▶ EIZO rugged GPGPU
- ▶ Elma's VITA 46.11 IPMI Tier 3 chassis manager
- ▶ Behlman Electronics VITA 62 PSU (optional)



Ethernet Performance Demo

Trend: Ethernet Moving from 1/10/40Gb to 1/10/25/40/100Gb



CompacFrame Development Platform Aligned to SOSA™ 1.0 with Ecosystem Plug-In Cards (PICs)

Explore true high-speed Ethernet performance across the entire system in this demonstration of what a true SOSA aligned ecosystem can do. Enabling more rapid development time and performance through partnerships.

3U 6-slot OpenVPX backplane

- › Slot profiles aligned to SOSA 1.0
- › Features VITA 67.3 RF and optical I/O modules for high-speed connectivity

Featuring the following Plug-In Cards (PICs)

- › Concurrent Technologies I/O intensive processor
- › Interface Concept Ethernet switch card
- › Elma VITA 46.11 IPMI Chassis Manager
- › Behlman Electronics VITA 62 PSU (optional)



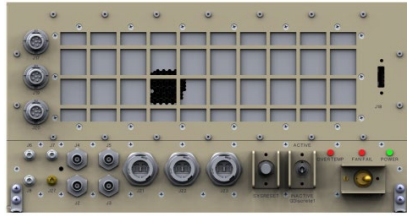
Ethernet Performance:

40 Gb - 2.8 GB/sec -> 100Gb - 5 GB/sec
RDMA - 9 GB/sec

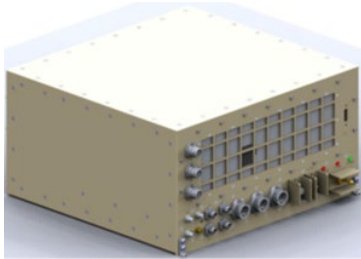
What's Next?

SAVE Aligned Chassis Progression

Existing SAVE
CMOSS ATR

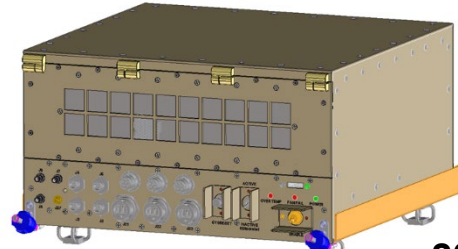


CMOSS

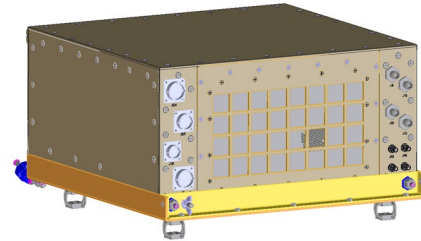


2022
Near Term

Ruggedized
CMFF ATR Test Chassis



2023
Next Gen CMFF



Now

Scalable, Sealed,
Rugged CMFF ATR

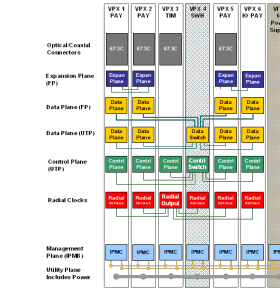
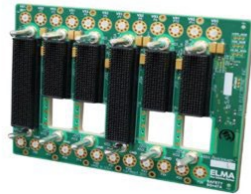


Notional

CMFF Aligned Backplanes

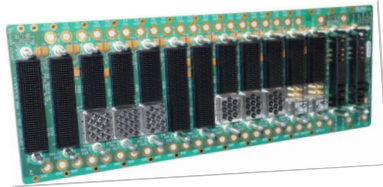
Scalable Backplane Architecture

SAVE format ATR
Small-Medium BP
7 to 11 slots

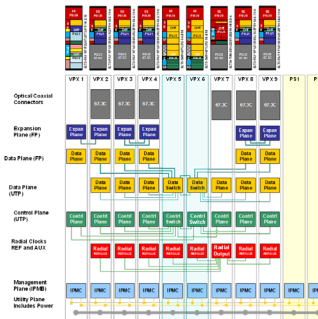


One VITA 62 PS

CMFF Large
format ATR
Medium-Large
BP 11 to 13



Six Slot + 1 PSU



Two VITA 62 PS

Elma CMOSS-2 backplane shown

Nine Slot + 2 PSU

- **Release SOSA 1.0** is enabling ecosystem to produce payload PICs, switches, power supplies and other cards to develop modular open systems
- **Active demos** can now be integrated in record time using aligned PICs and Reference Chassis
- **Service branches** are adopting and beginning to deploy available product developed in alignment to the SOSA 1.0 Technical Standard
- **Benefits** of adopting MOSA is being proven by the increase of vendors producing products to support the effort
- **Over 50 vendors** demonstrated products at 2022 FACE / SOSA TIM & Expo
- **Stay tuned** for another exciting chapter from the Book of MOSA...



12th c. Mosa Psalter fragment

Thank you
for your
time!

Questions?